ELECTONICALLY FILED ON \(\lambda \) OCTOBER 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application

Inventor(s): Albert Sun et al.

Appl. No.: 10/699,756

Confirm. No.: 4258

Filed:

03 November 2003

Title: In-Circuit Configuration Architecture

WITH CONFIGURATION ON INITIALIZATION FUNCTION FOR EMBEDDED CONFIGURABLE

LOGIC ARRAY

Group Art Unit:

2186

Examiner:

Chase W. Peers

Customer No. 22470

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

REQUEST FOR CORRECTED FILING RECEIPT

Sir:

Attached is a copy of the official Filing Receipt marked in red to identify the correction needed. The correction is as follows:

Please change the third inventor Shih-Liang Chen's residence from "Hsinchu, Taiwan" to -

Fengyuan, Taiwan --.

Since the error was not caused by applicant, but by the Patent Office, no fee is believed necessary. However, the Commissioner is hereby authorized to charge any additional fees which may be required by this paper, or to credit any overpayment to Deposit Account No. 50-0869 (MXIC 1521-1).

Respectfully submitted,

Dated: 10 Det DL

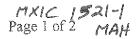
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FILING OR 371 DRAWINGS IND CLMS FIL FEE REC'D ATTY.DOCKET NO TOT CLMS ART UNIT APPL NO. (c) DATE 17 MXIC 1521-1 770 10/699,756 11/03/2003 2811

CONFIRMATION NO. 4258

22470 HAYNES BEFFEL & WOLFELD LLP P O BOX 366 HALF MOON BAY, CA 94019

FILING RECEIPT

OC000000011850212

Date Mailed: 02/06/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Albert Sun, Hsinchu, TAIWAN; Eric Sheu, Hsinchu, TAIWAN; Shih-Liang Chen, Hsinchu, TAIWAN; rengyuan

Assignment For Published Patent Application

MACRONIX INTERNATIONAL CO., LTD., HSINCHU, TAIWAN;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 02/05/2004

Projected Publication Date: 05/05/2005

HAYNES BEFFEL & WOLFELD LLP

Non-Publication Request: No

FEB 0 9 2004

Early Publication Request: No

RECEIVED

Title

In-circuit configuration architecture with configuration on initialization function for embedded configurable logic array

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